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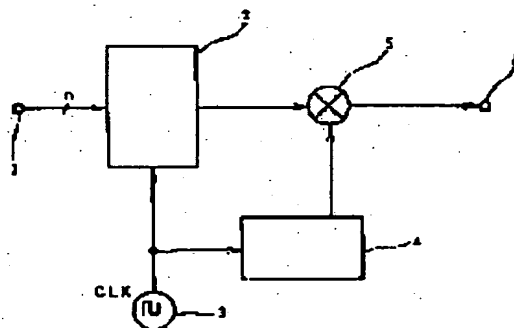
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(54) D/A CONVERTER

(57)Abstract:

PURPOSE: To facilitate large scale integration, to make the D/A converter small and to decrease power consumption by adopting simple circuit configuration comprising a correction pulse generating circuit generating a desired correction pulse in response to a clock signal and multiplier only so as to correct deterioration in amplitude due to the aperture effect.

CONSTITUTION: An n-bit digital signal is applied to a digital signal input terminal 1 and given to a D/A converter 2. An output of the circuit 2 is a PAM signal pulse train whose analog property duty factor is 100% and the period of which is matched with that of a clock signal CLK. Thus, a frequency characteristic of the PAM signal being the output of the circuit 2 is a characteristic in which Fourier transformation of each isolated rectangular pulse of the PAM signal is applied to the frequency characteristic of a source analog signal. On the other hand, a correction pulse output with a prescribed waveform is generated from a correction pulse generating circuit based on an output of a clock signal generator 3 and given to a multiplier 5. The deterioration in the amplitude due to aperture effect is corrected through the multiplication of an output of the circuit 2 by the correction pulse.



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